

FIG. 1

FIG. 2

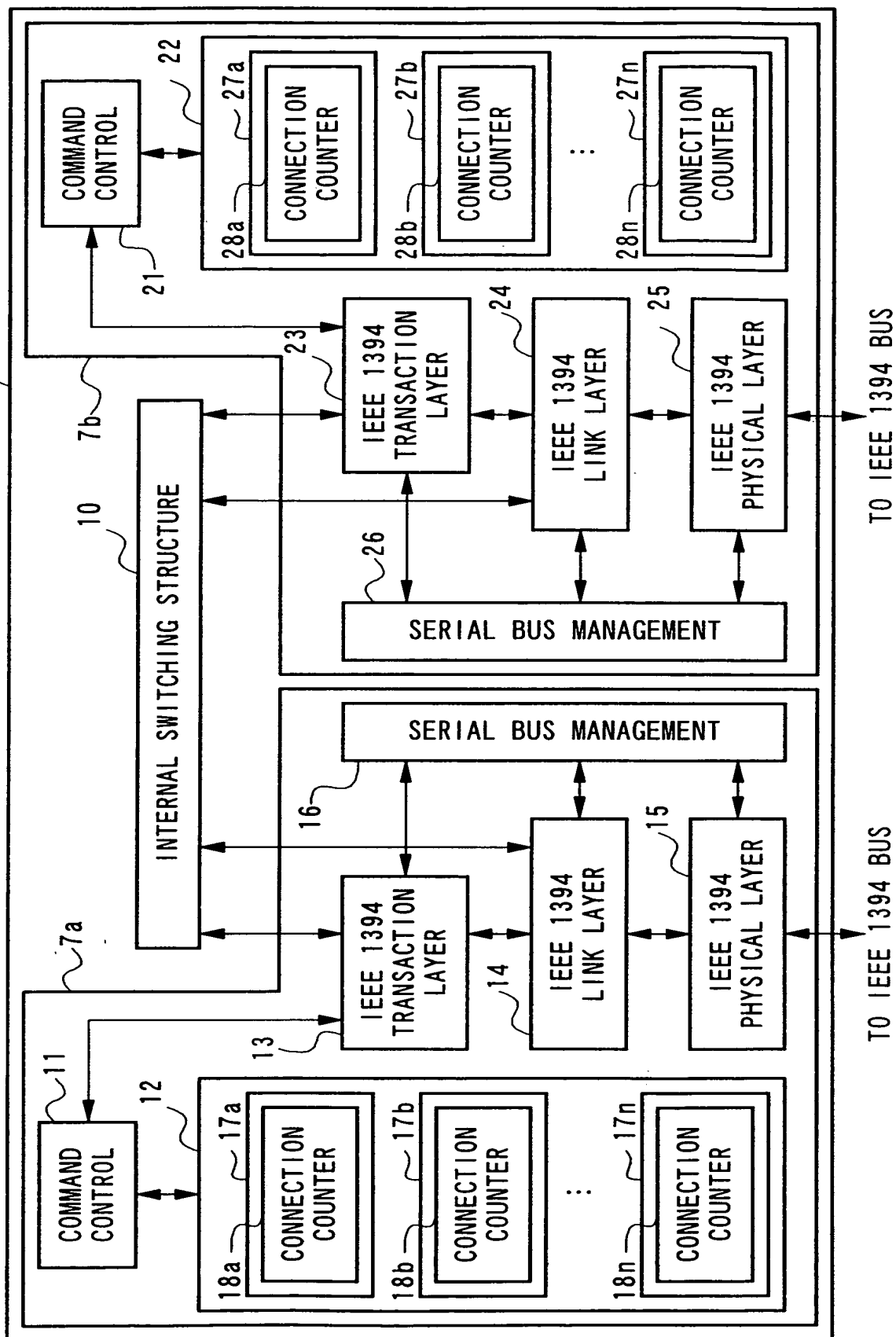
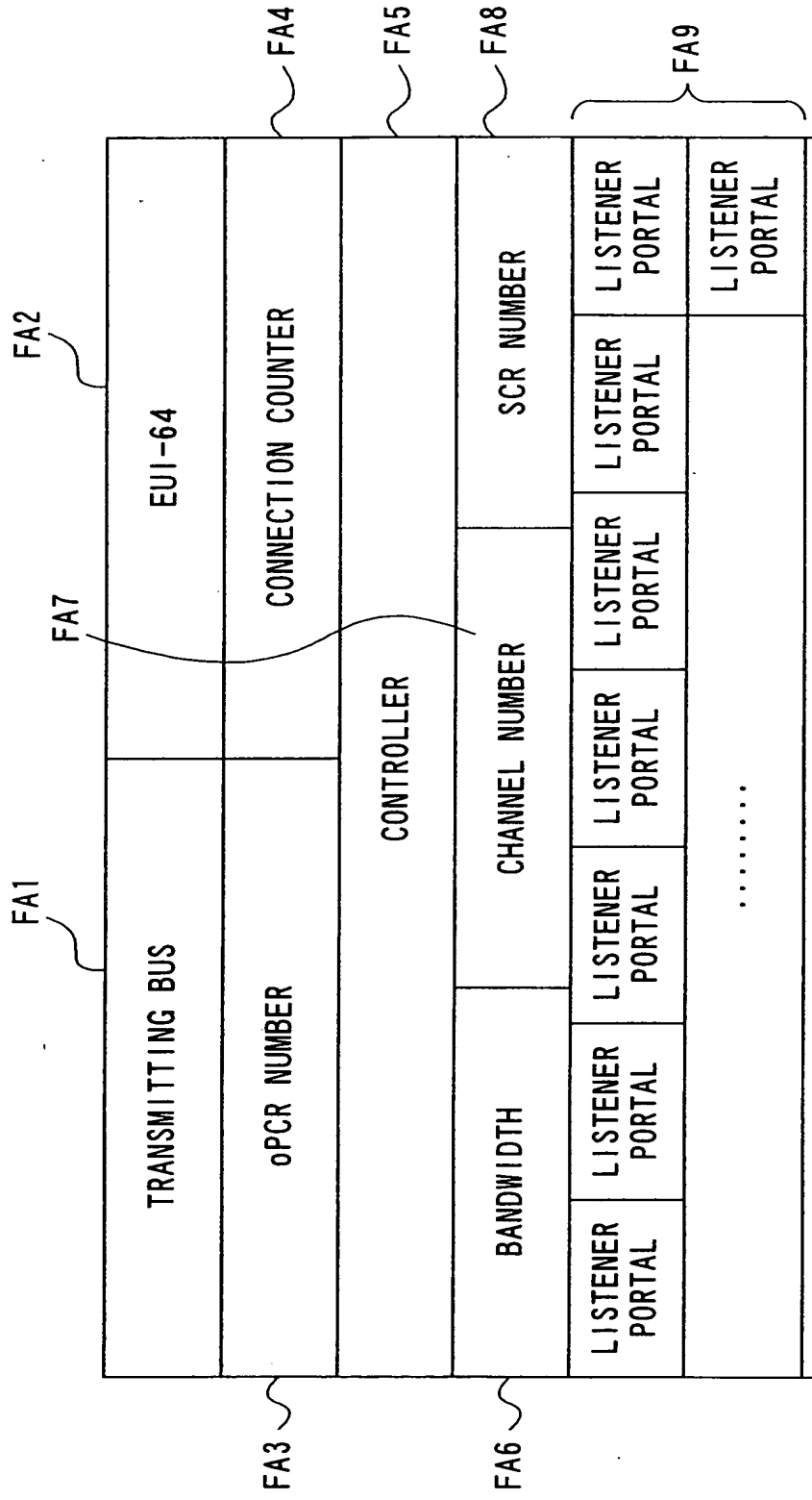


FIG. 3



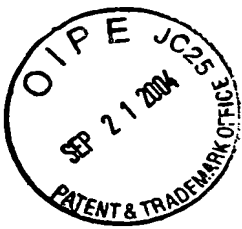
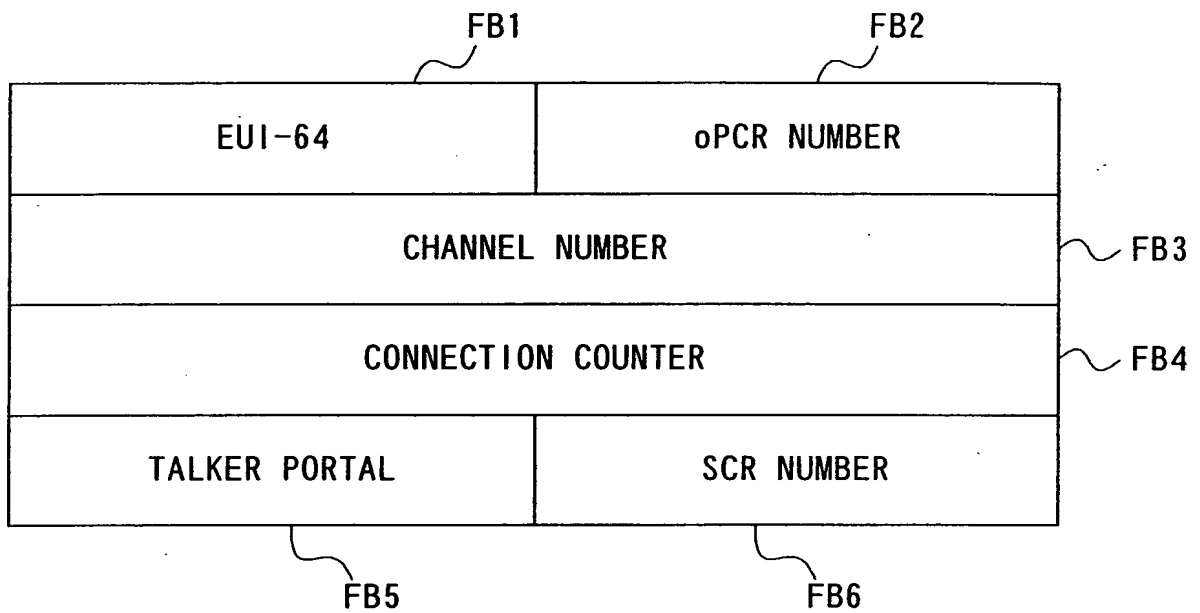


FIG. 4



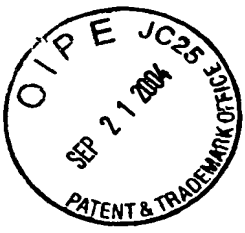


FIG. 5

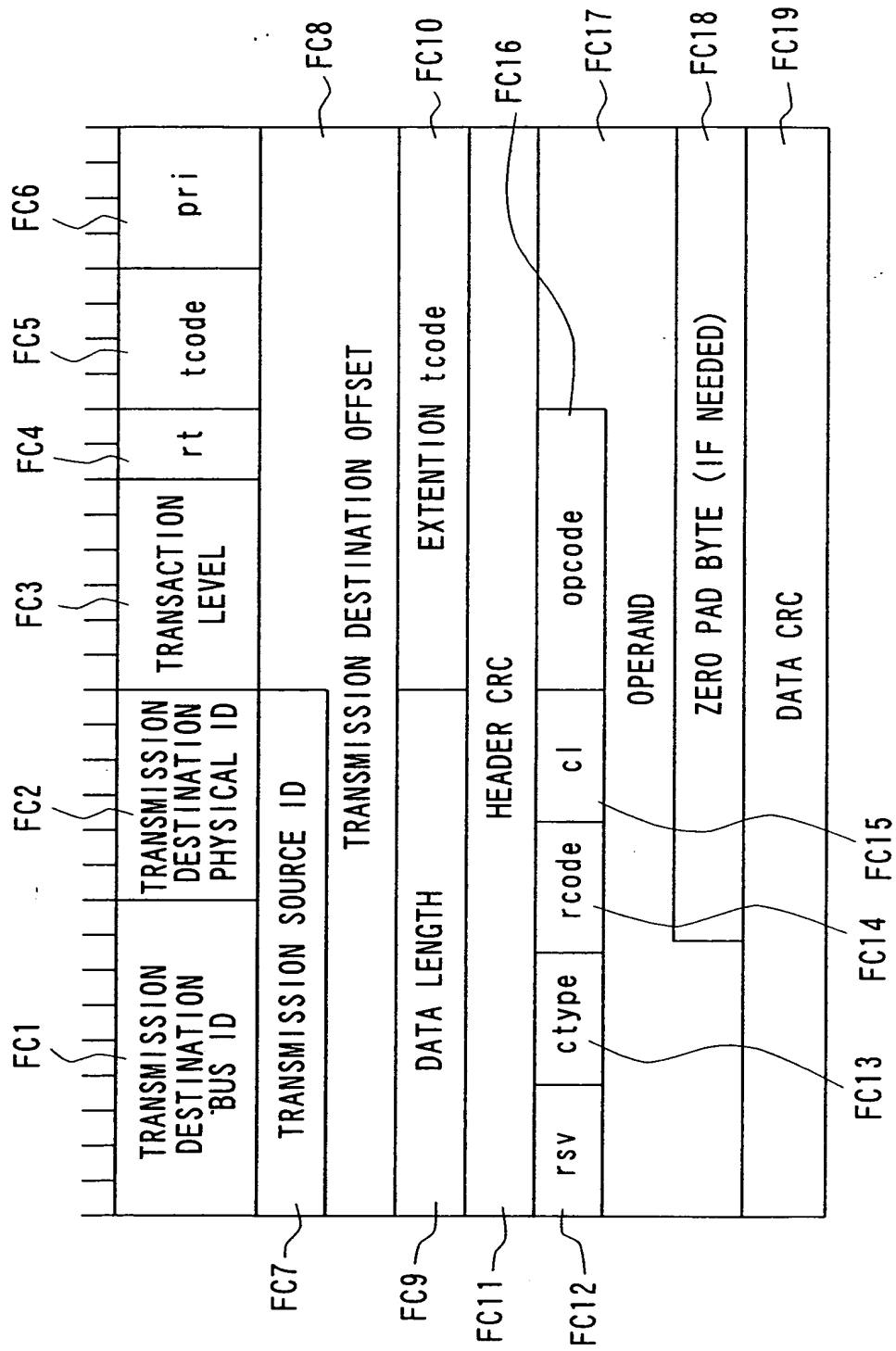
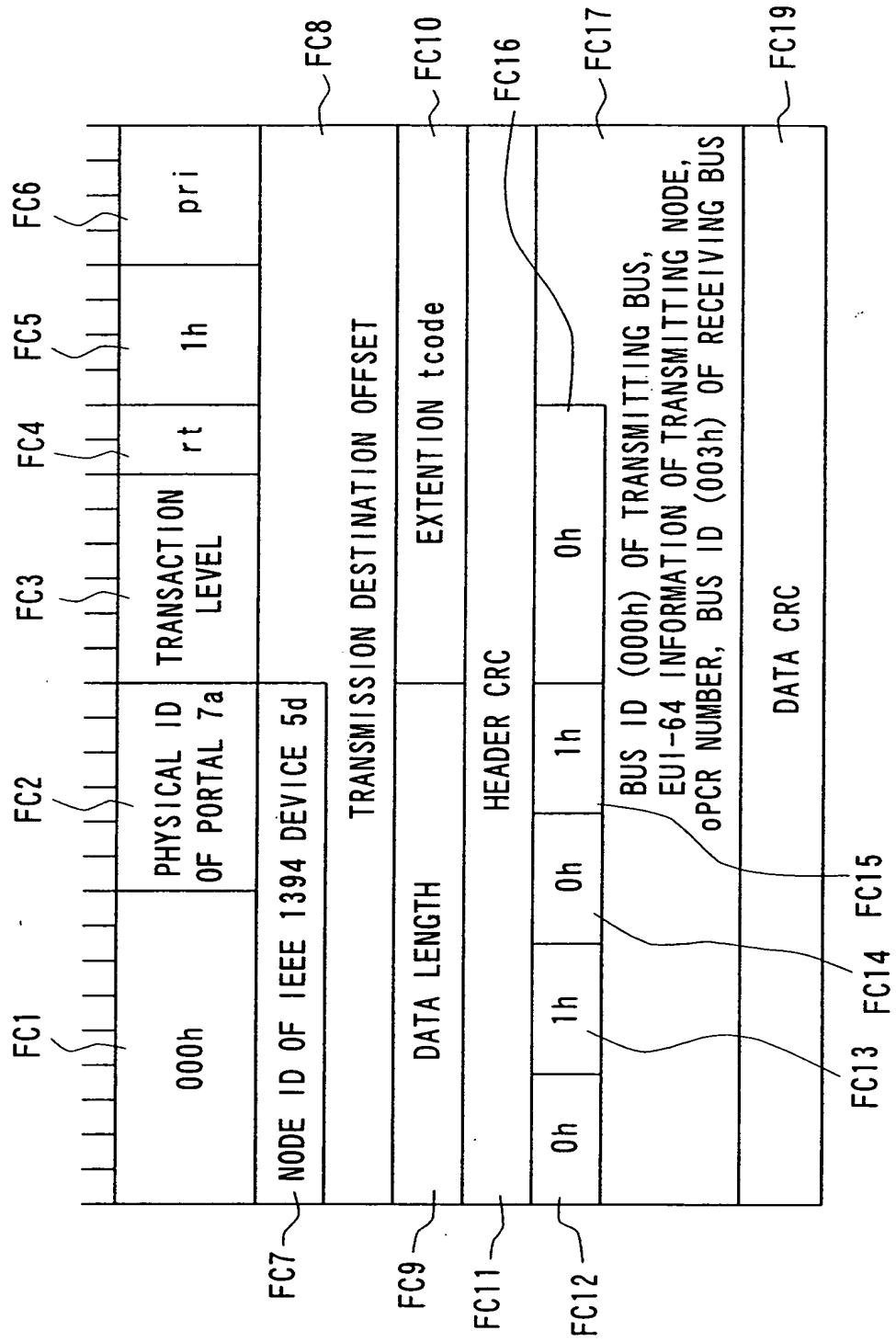


FIG. 6



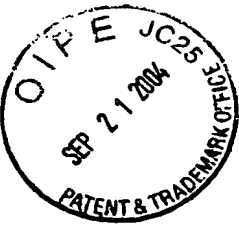


FIG. 7

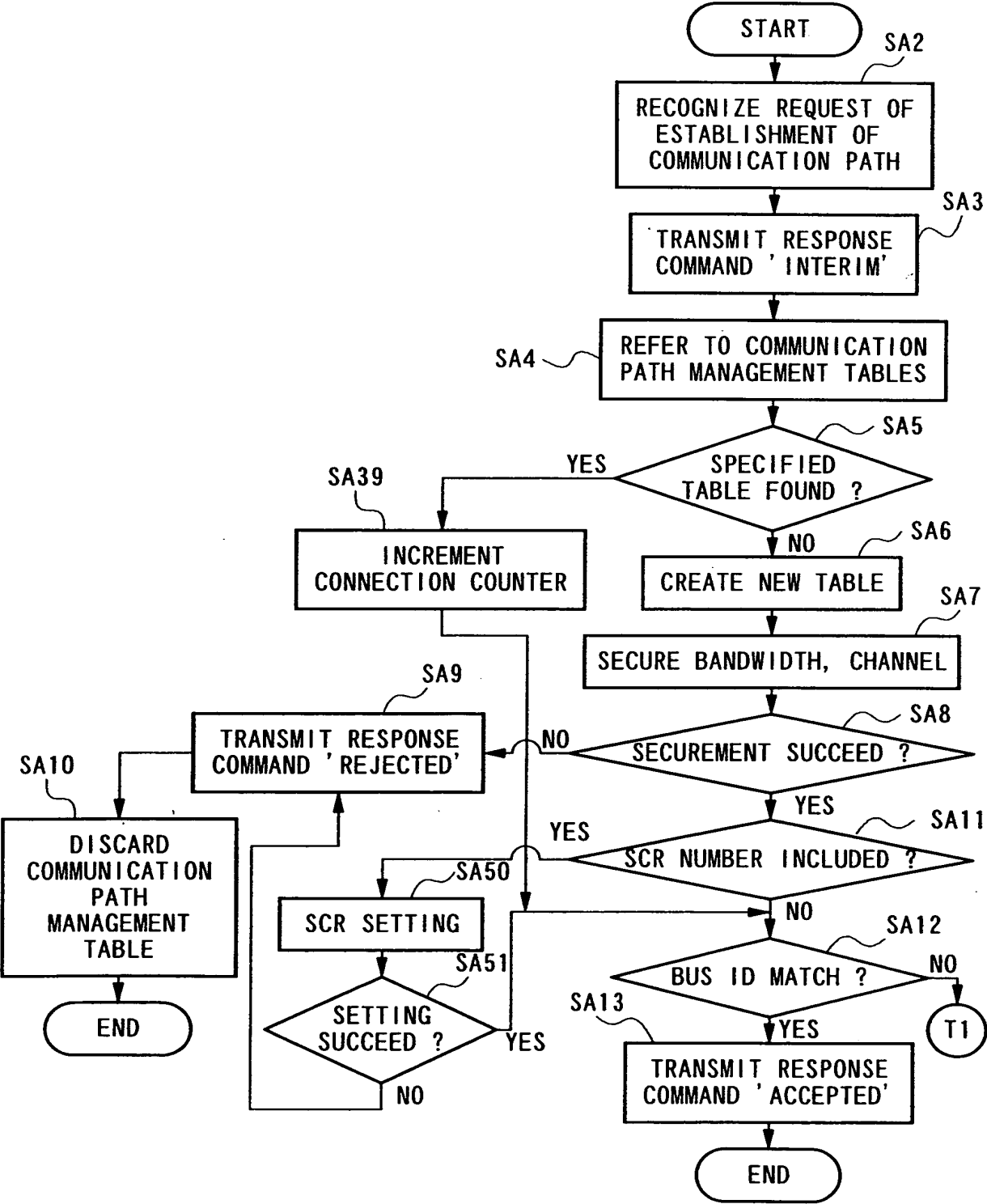


FIG. 8

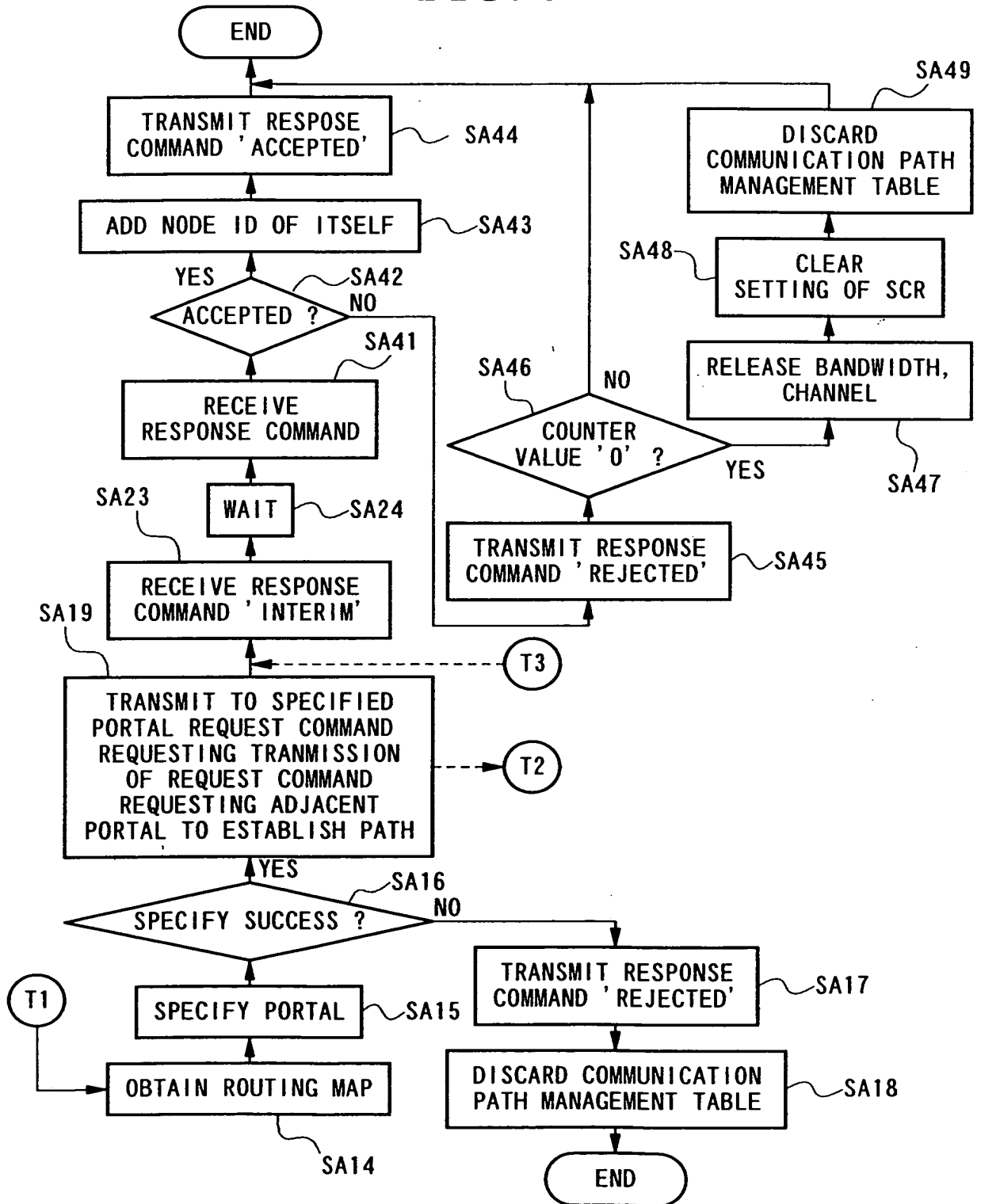


FIG. 9

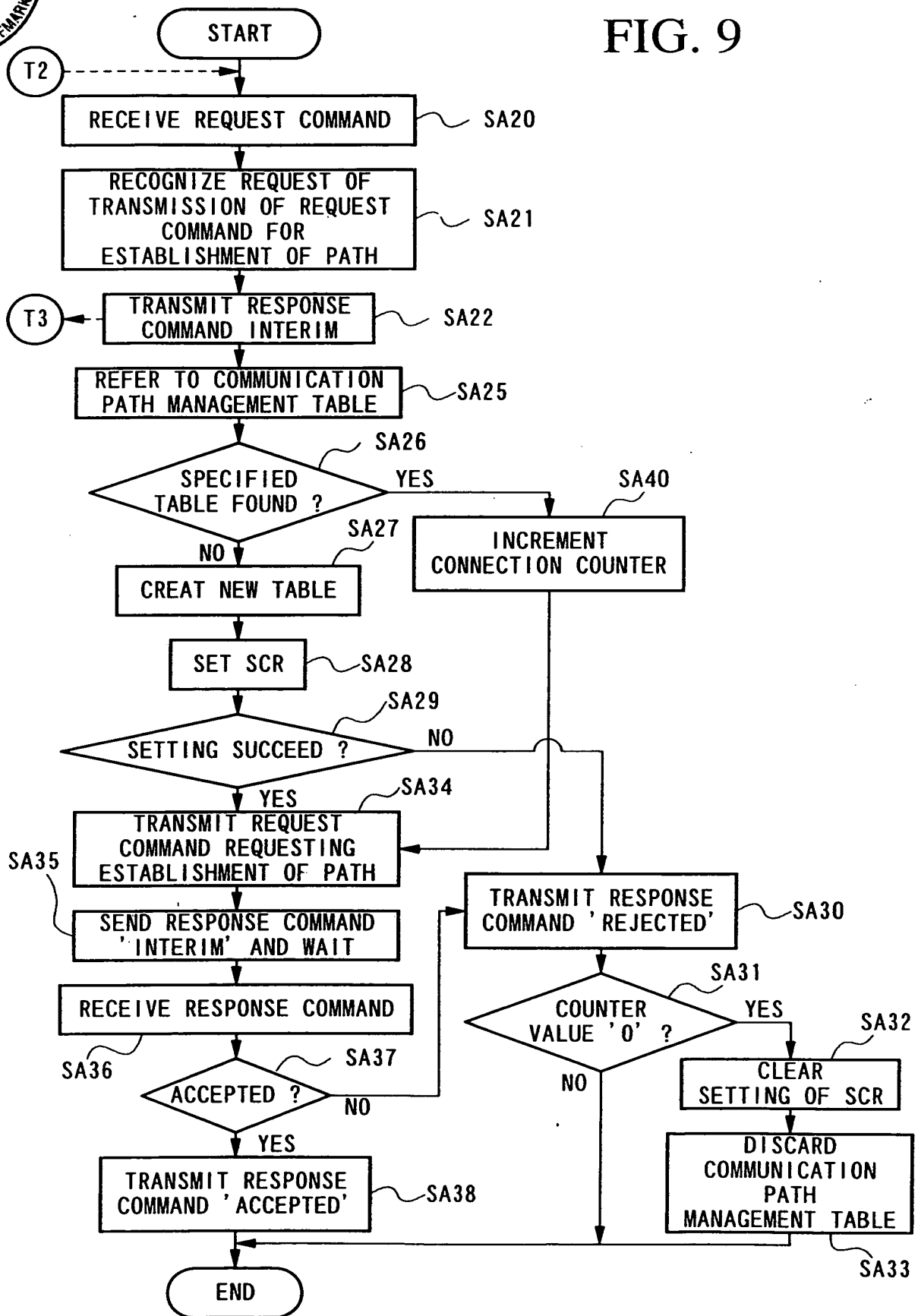


FIG. 10

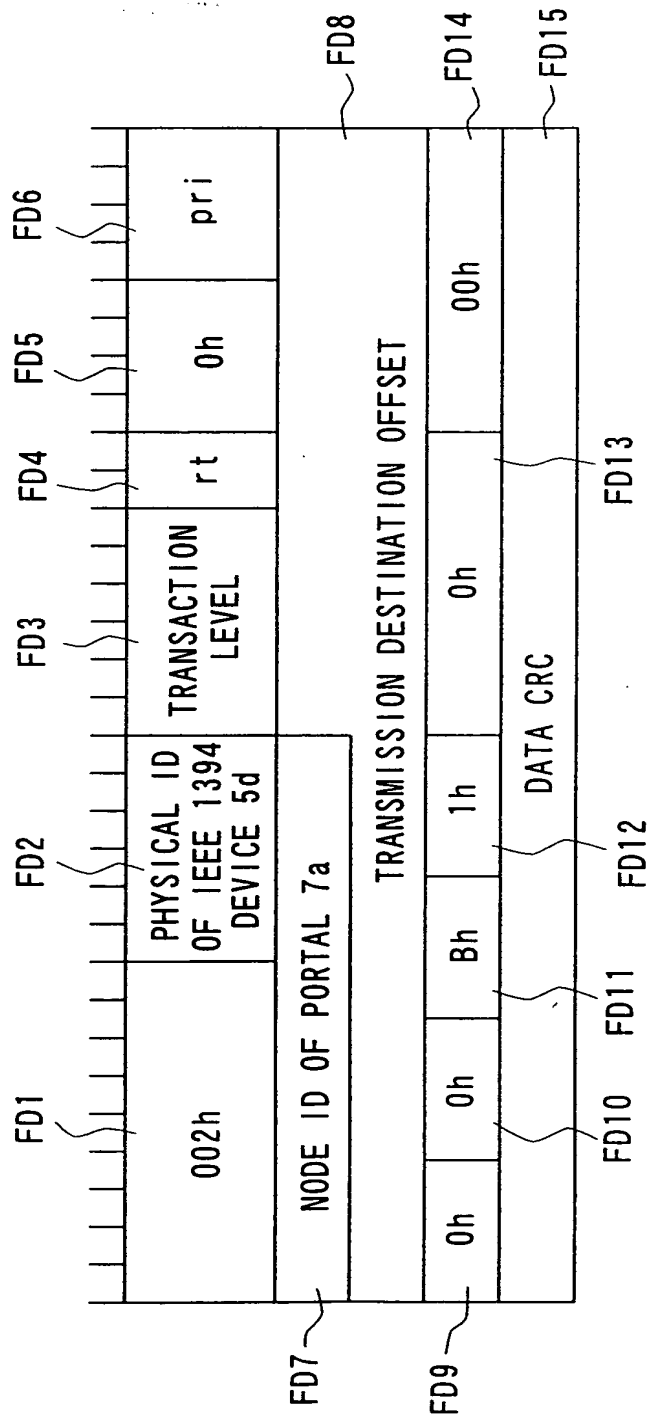


FIG. 11

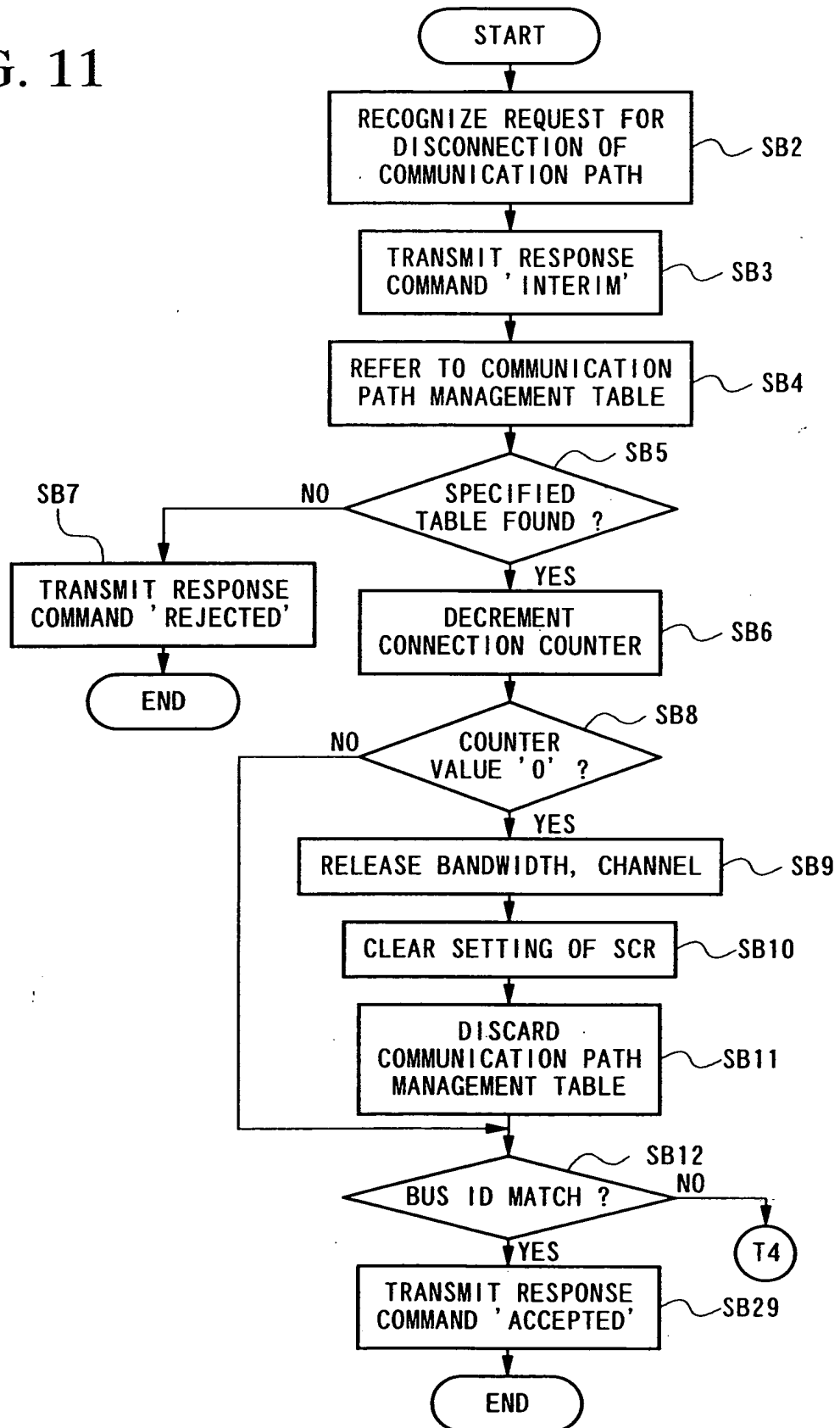


FIG. 12

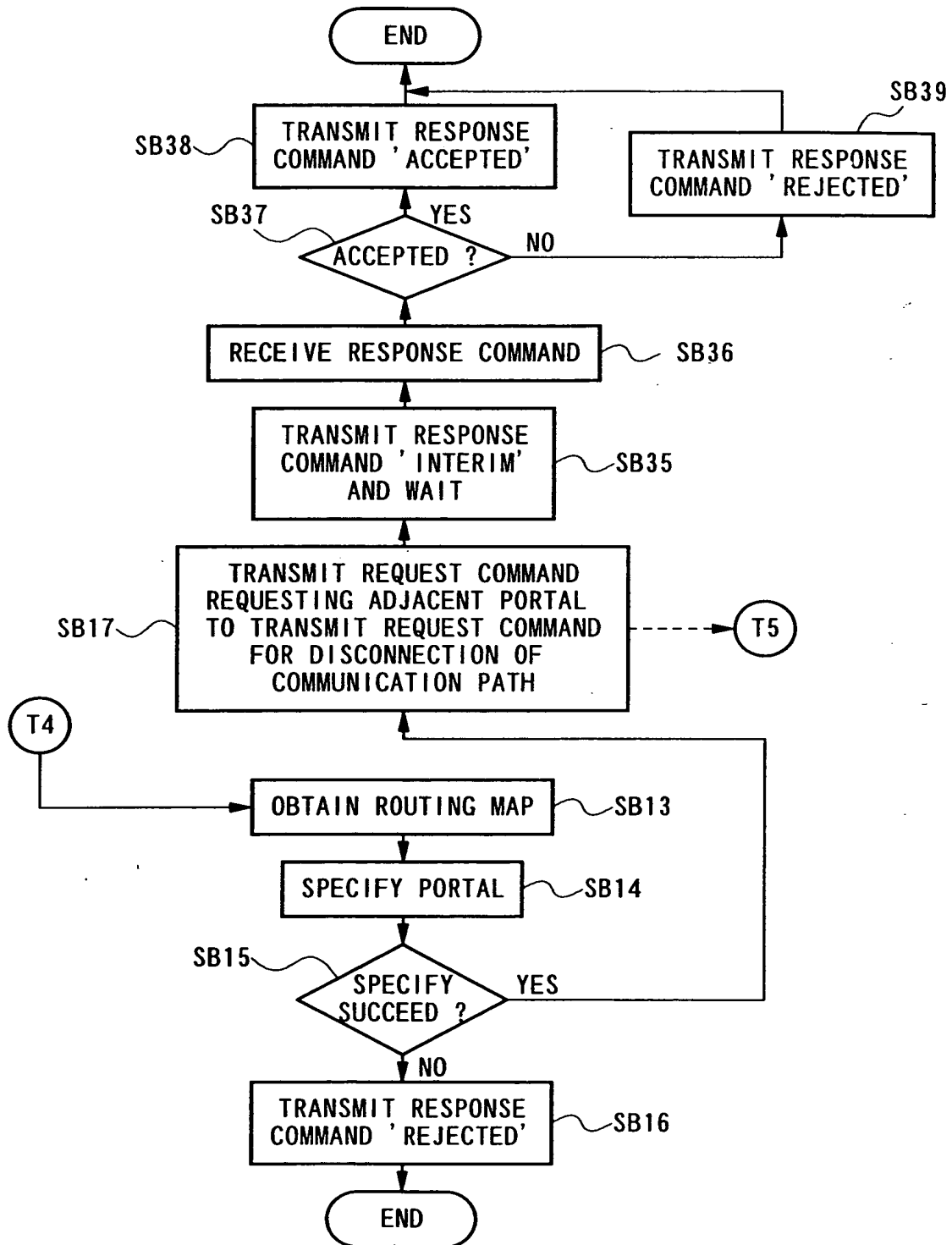


FIG. 13

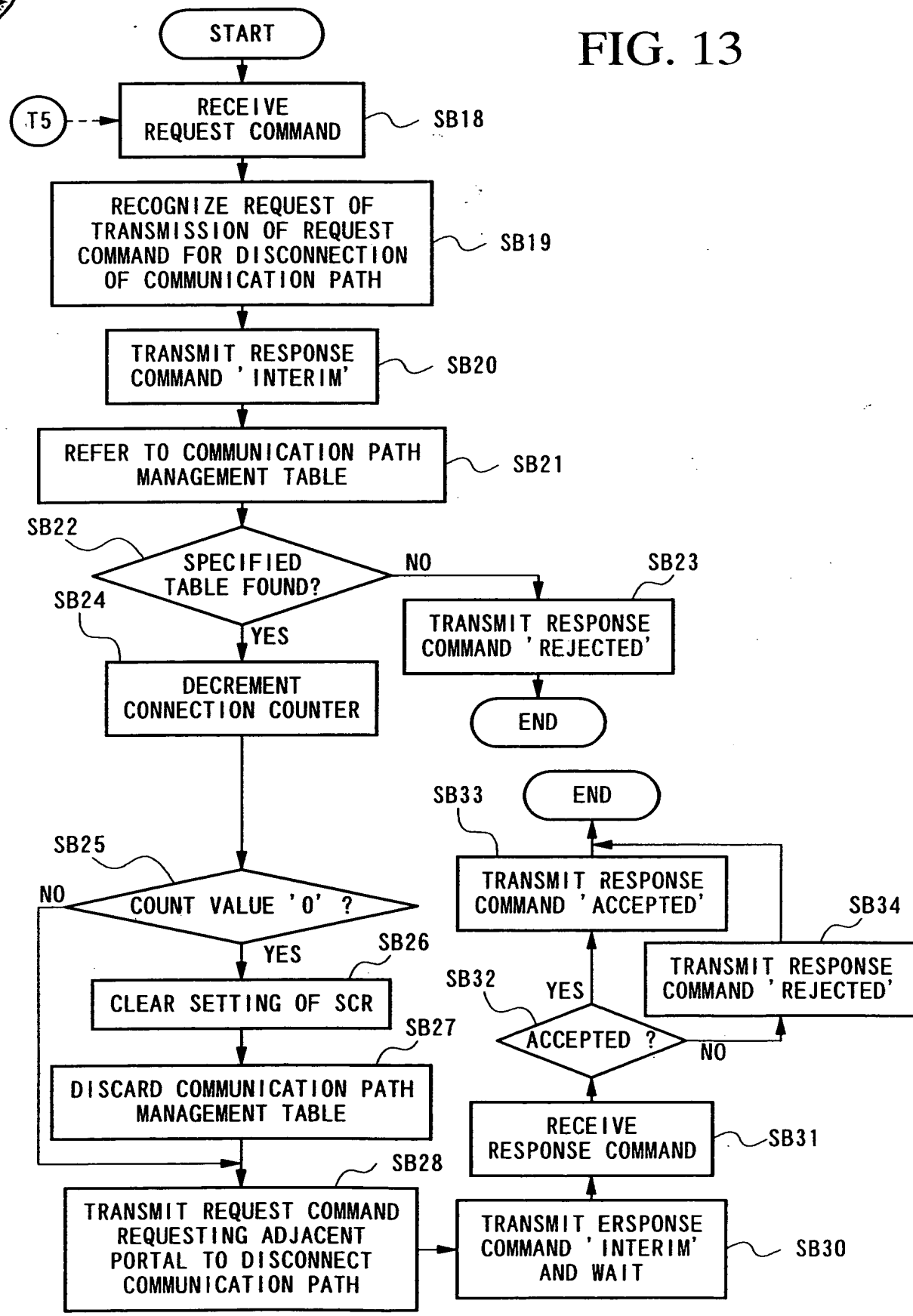


FIG. 14A

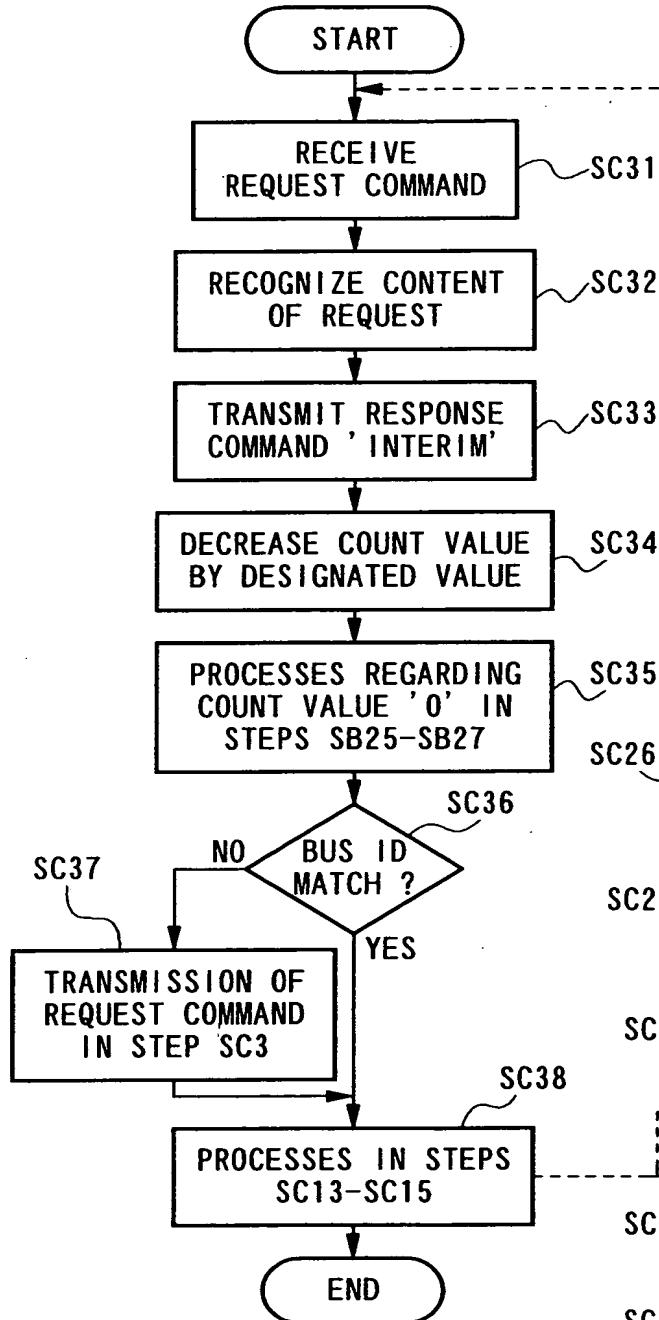


FIG. 14B

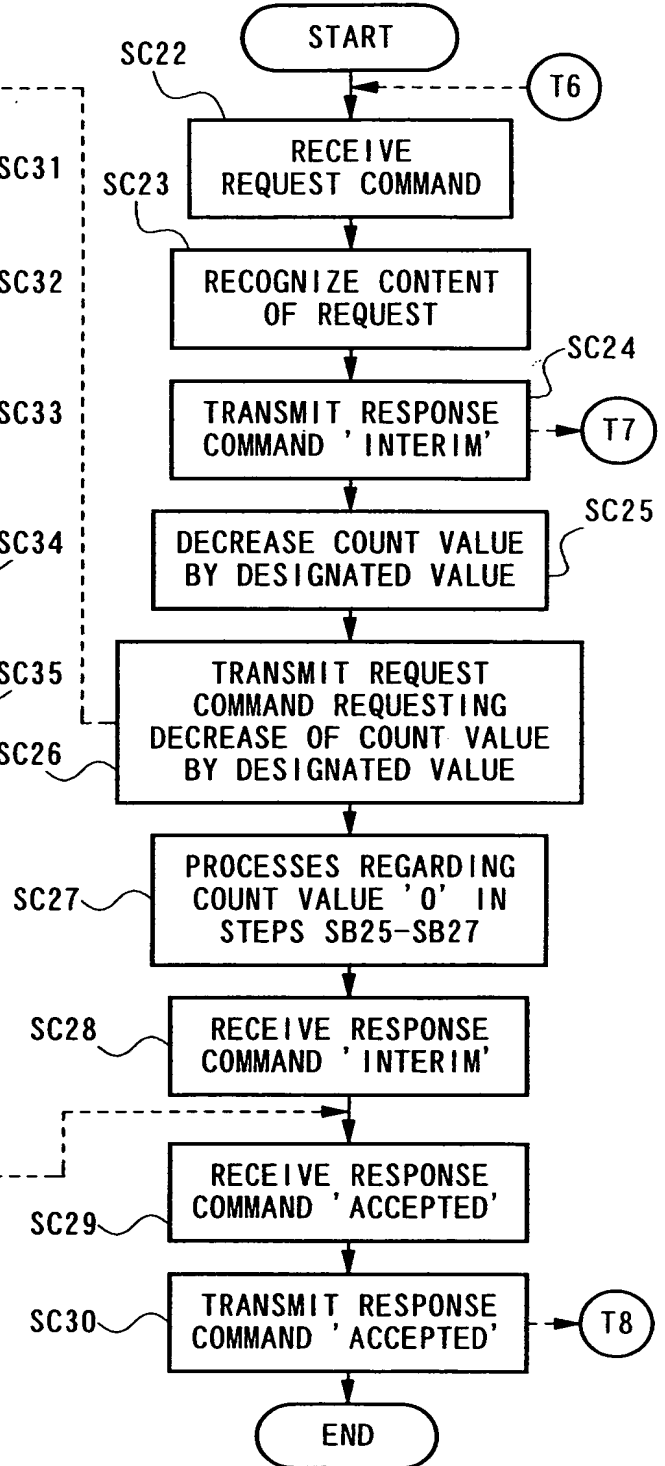


FIG. 15A

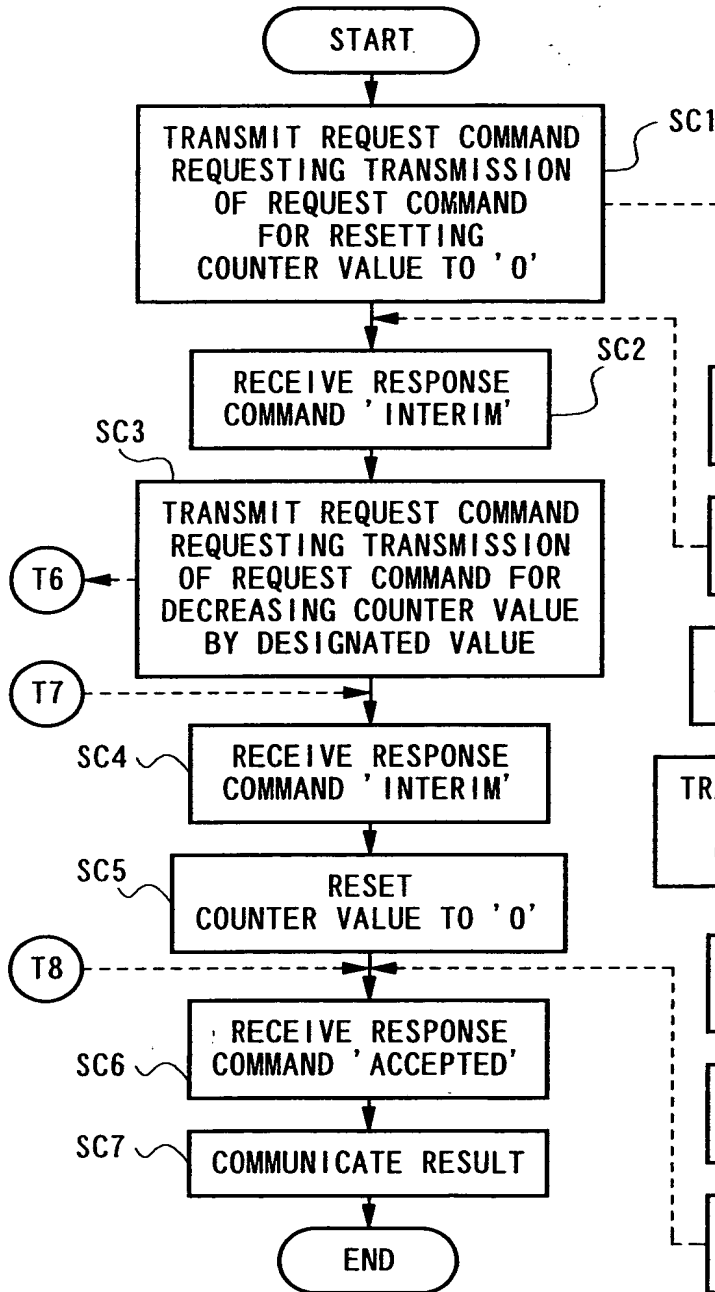


FIG. 15B

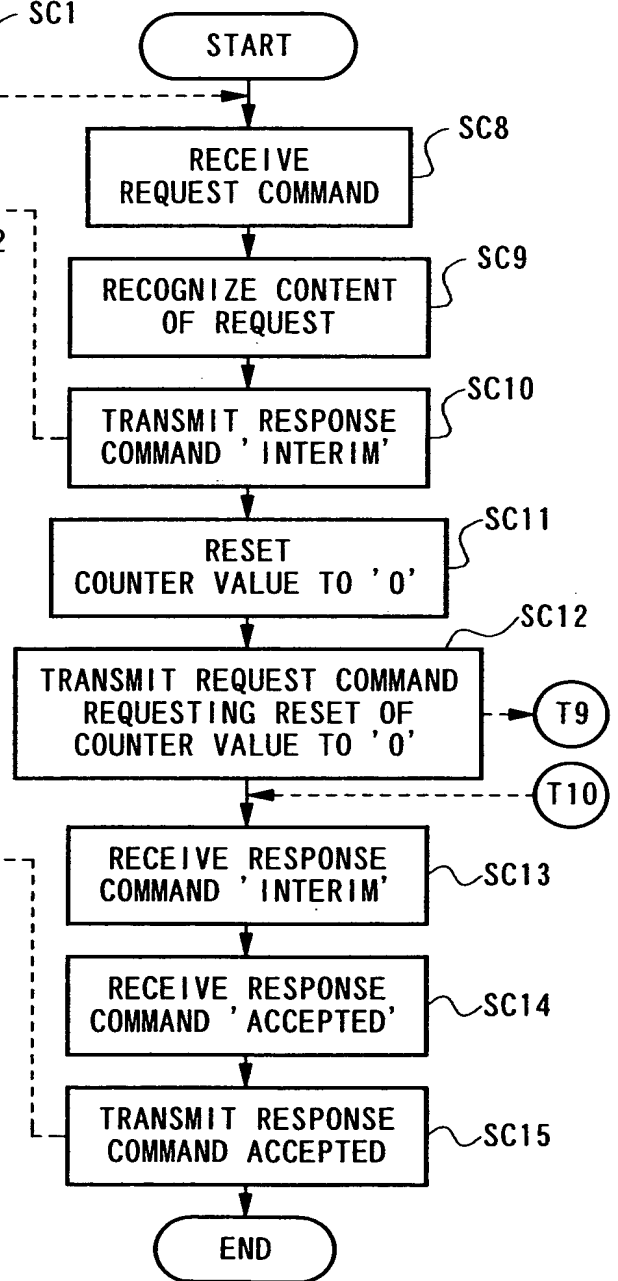


FIG. 16

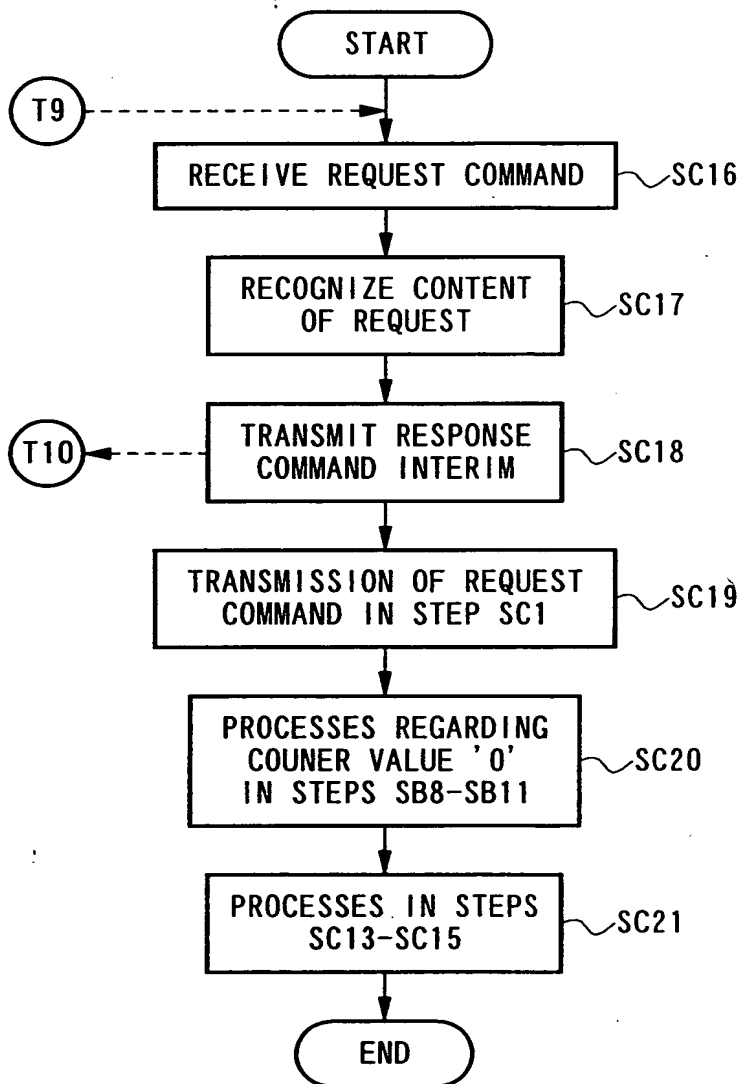


FIG. 17

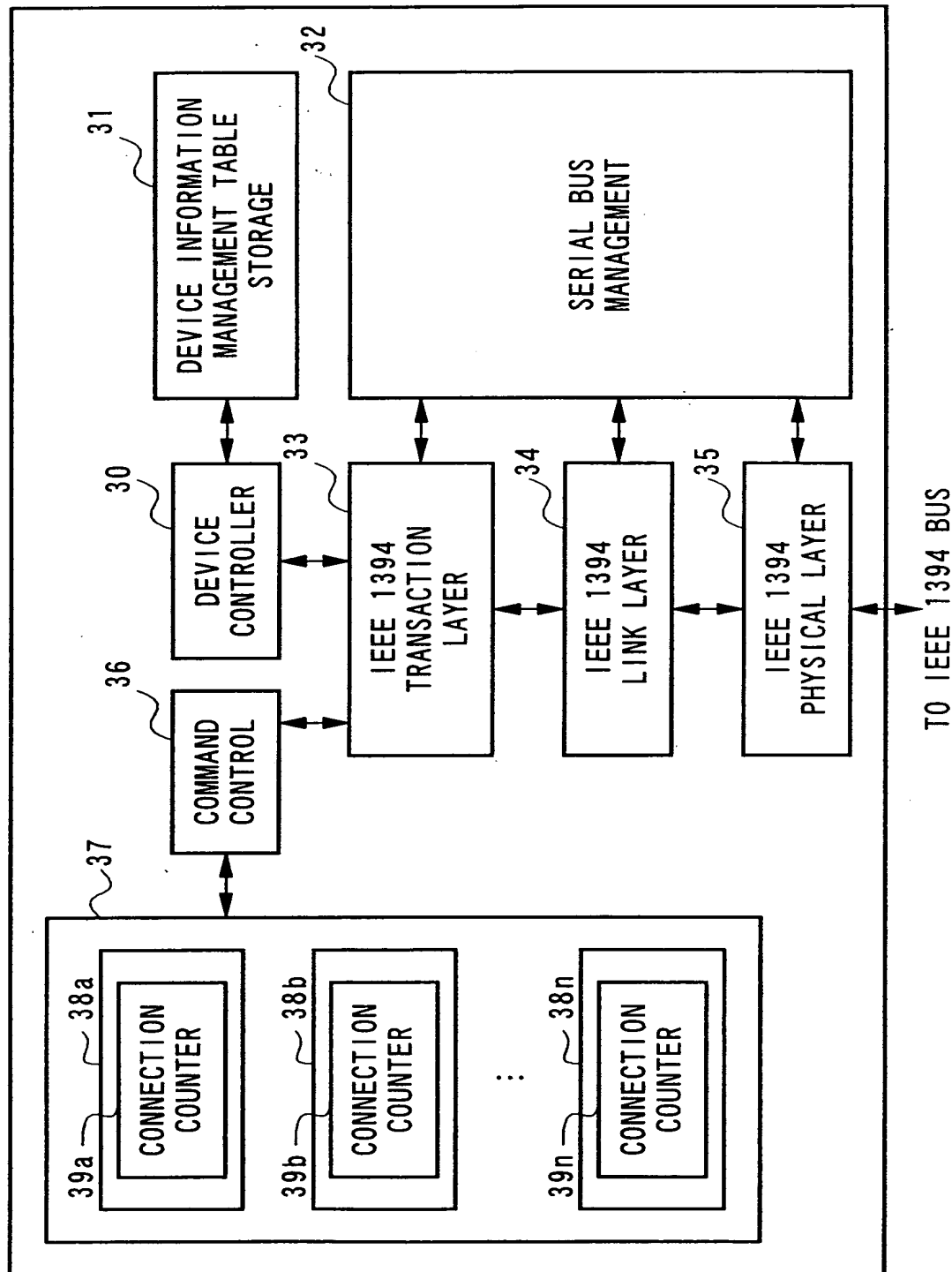


FIG. 18

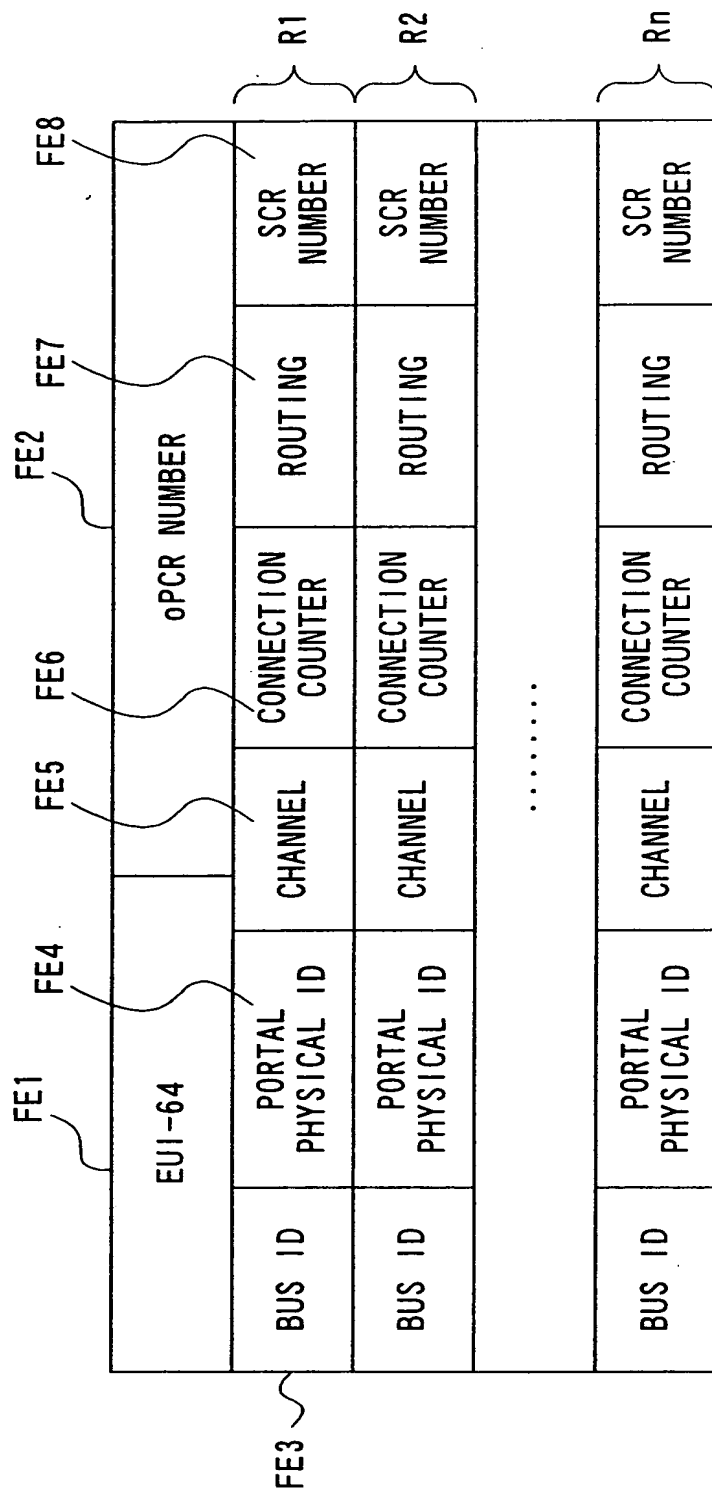


FIG. 19

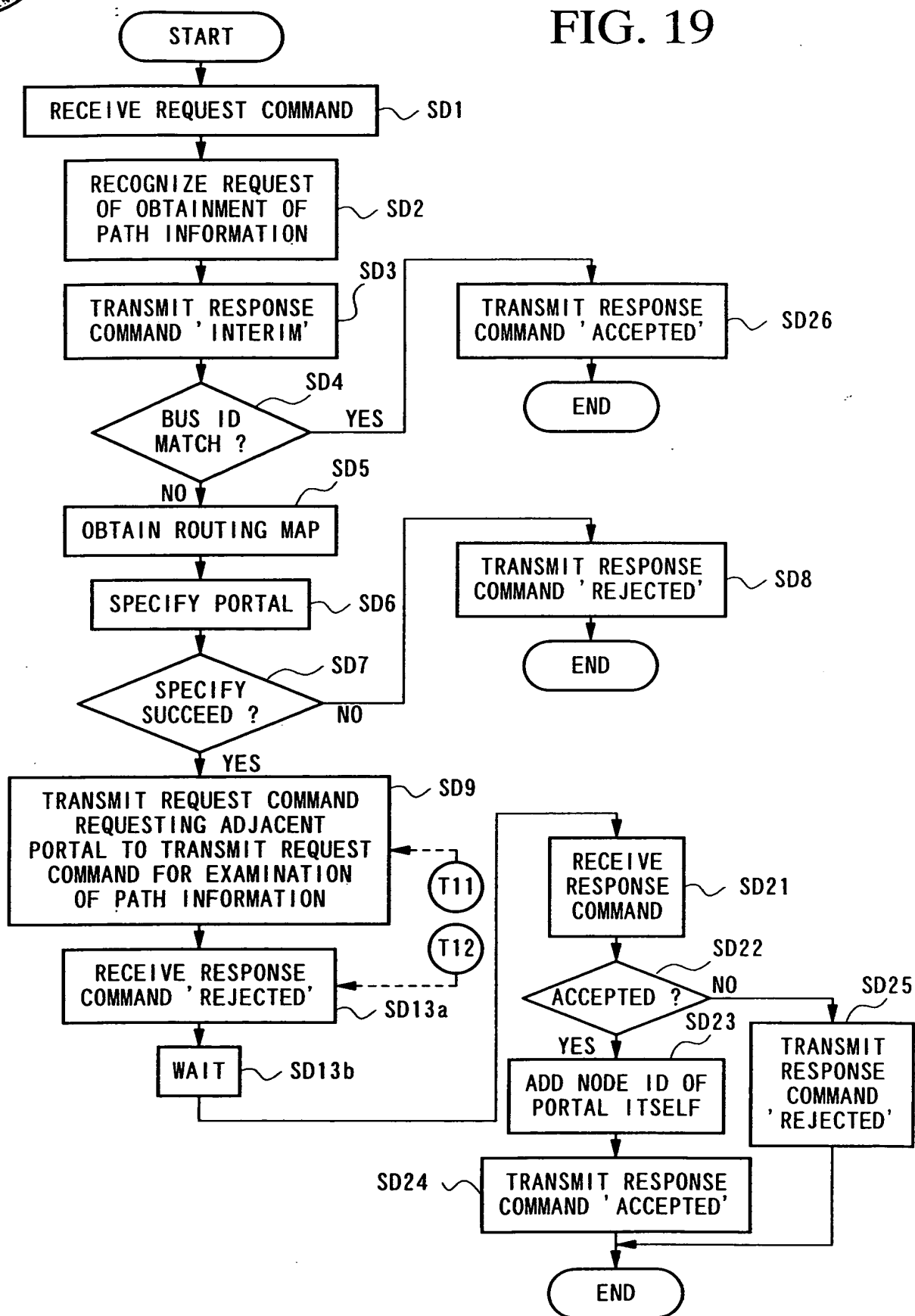




FIG. 20

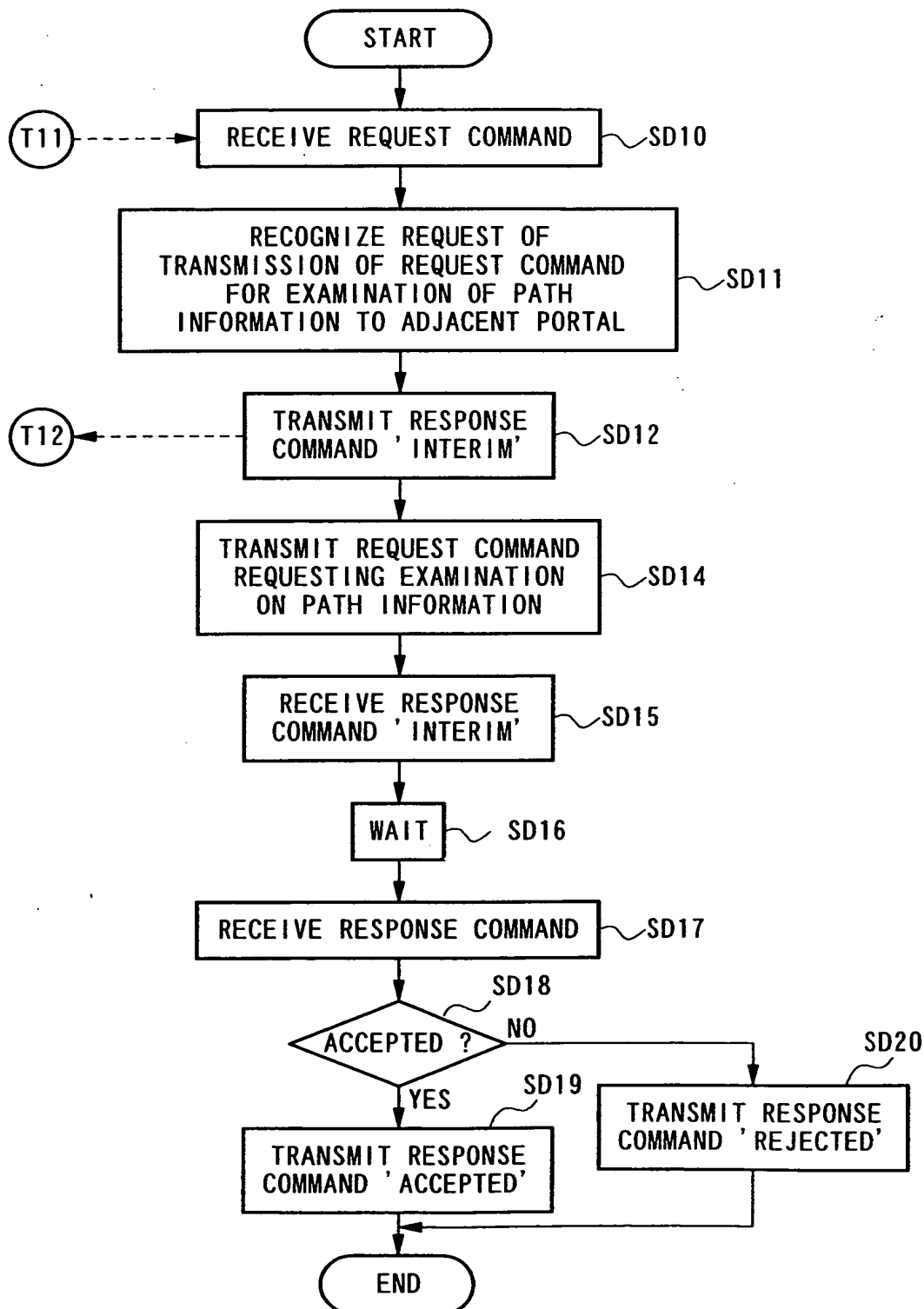


FIG. 21

EUI-64 INFORMATION OF IEEE 1394 DEVICE 3b				oPCR NUMBER		FE7		FE8
FE3	FE1	FE4	FE5	FE6				
3 (BUS ID OF BUS 1d)	PHYSICAL ID OF PORTAL 9a	5 (SECURED CHANNEL NUMBER)	1 (CONNECTION COUNTER)	NODE ID OF PORTAL 7b	SCR NUMBER (SETTING IMCOMPLETE)	R1		
1 (BUS ID OF BUS 1b)	PHYSICAL ID OF PORTAL 7b	1 (SECURED CHANNEL NUMBER)	1 (CONNECTION COUNTER)	NODE ID OF PORTAL 7a	SCR NUMBER (SETTING IMCOMPLETE)	R2		
0 (BUS ID OF BUS 1a)	PHYSICAL ID OF PORTAL 7a	3 (SECURED CHANNEL NUMBER)	1 (CONNECTION COUNTER)		SCR NUMBER (SETTING IMCOMPLETE)	R3		



FIG. 22
PRIOR ART

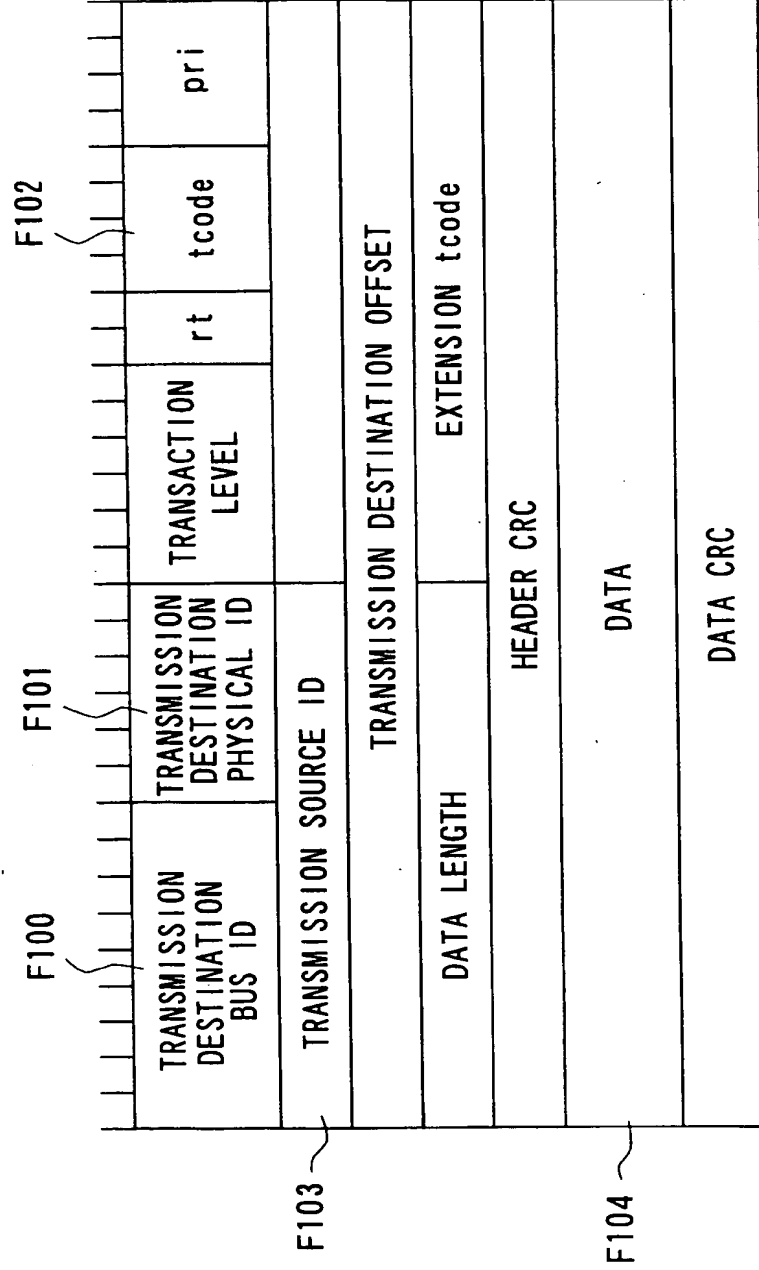




FIG. 23
PRIOR ART

F110

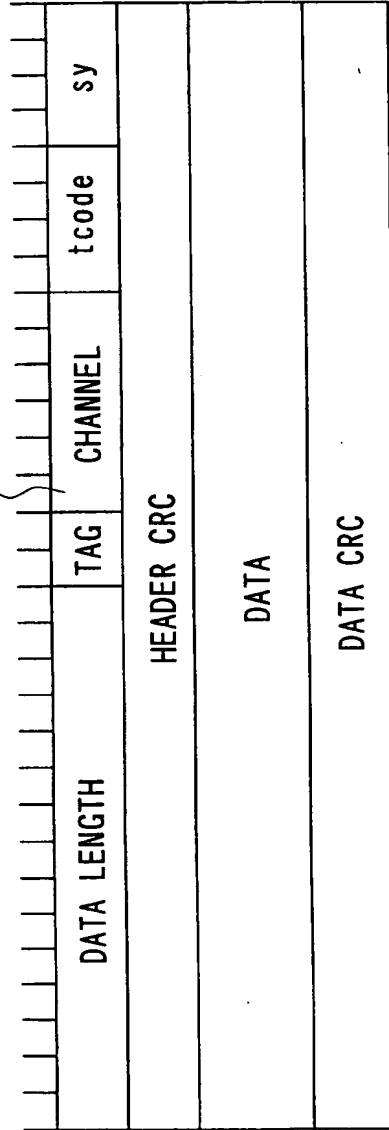




FIG. 24
PRIOR ART

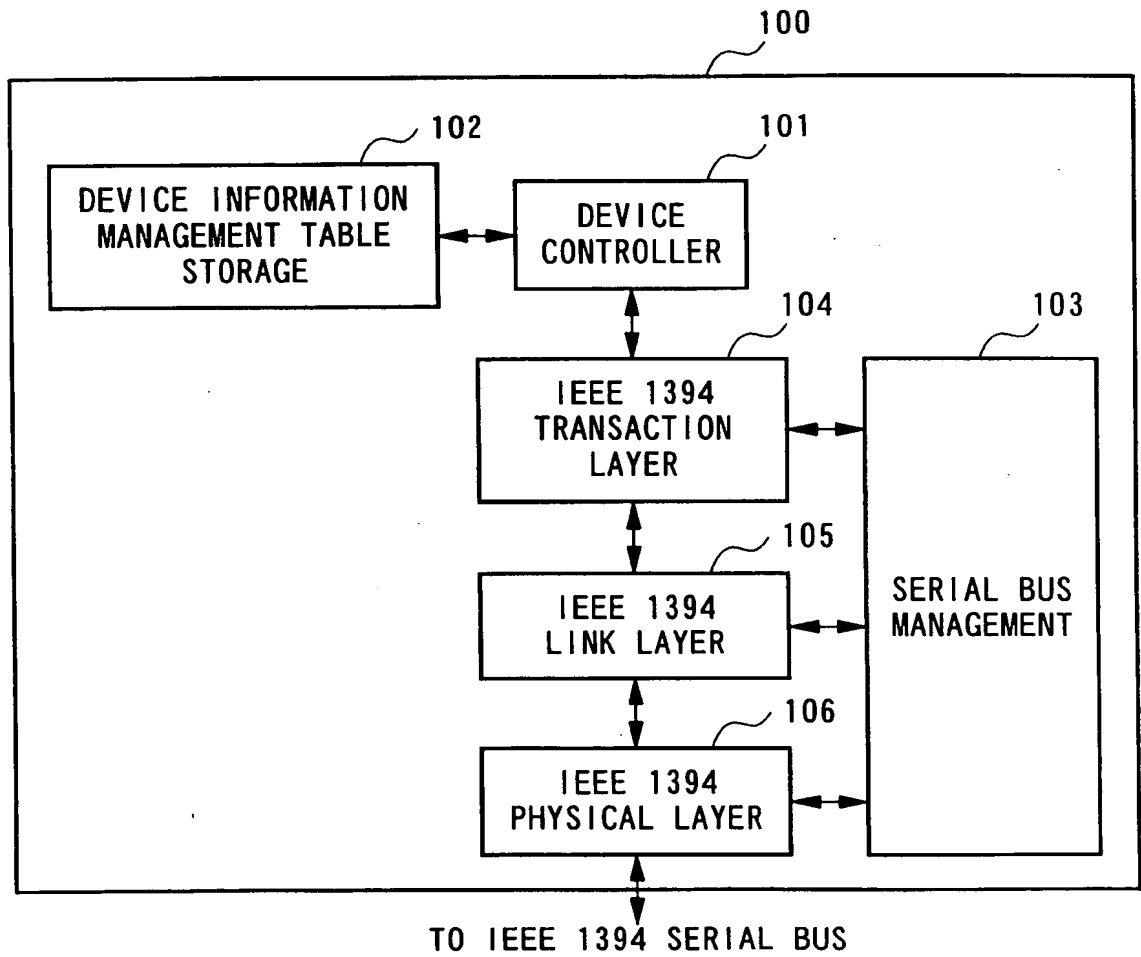


FIG. 25A
PRIOR ART

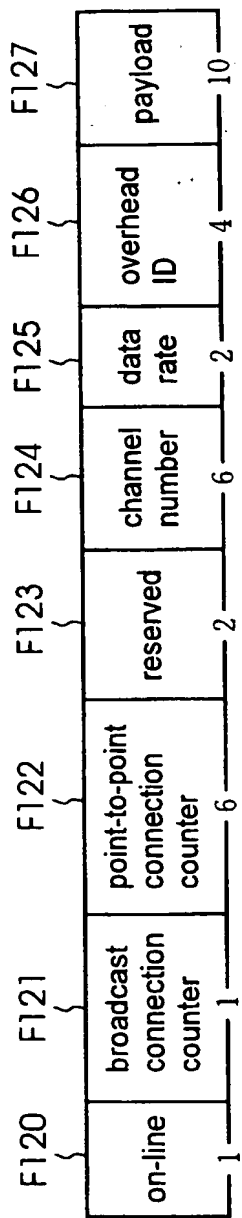


FIG. 25B
PRIOR ART

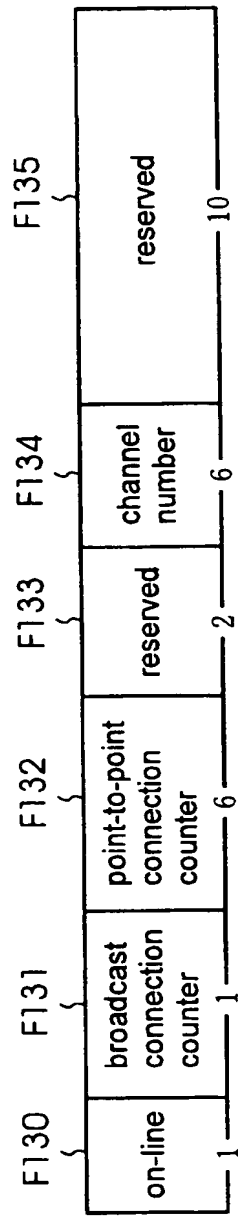


FIG. 26
PRIOR ART

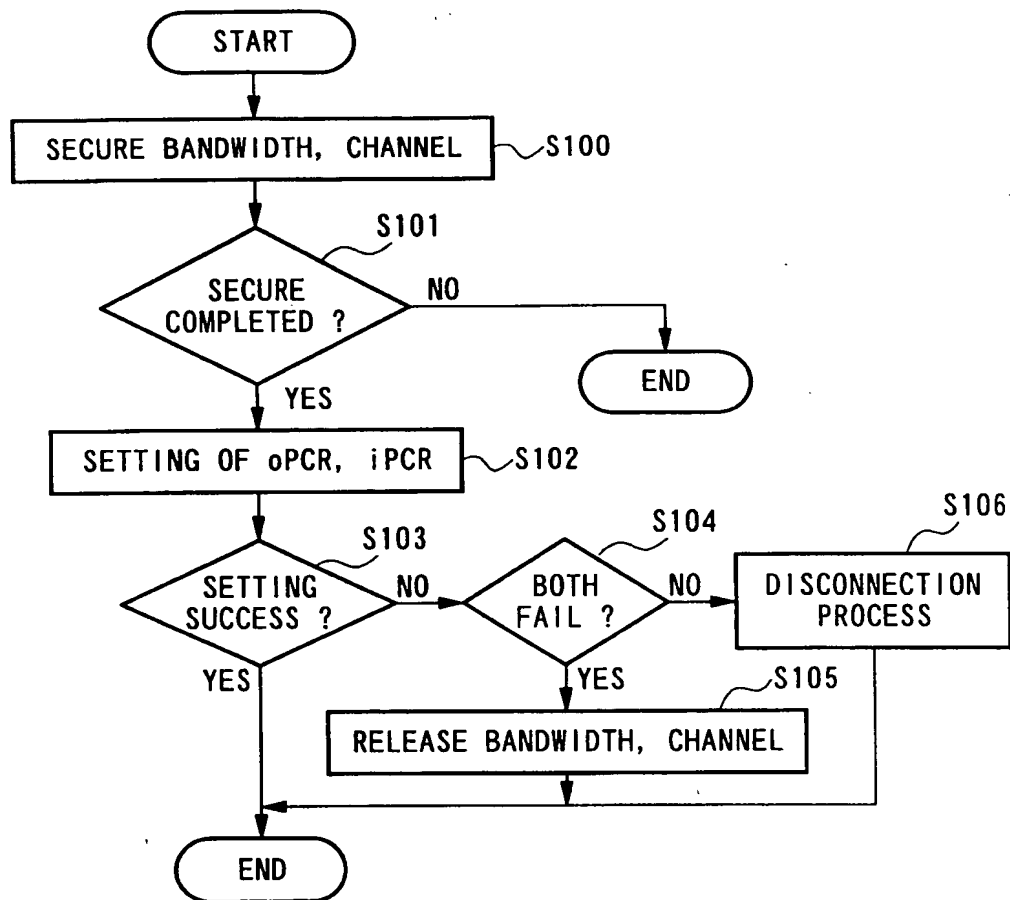


FIG. 27
PRIOR ART

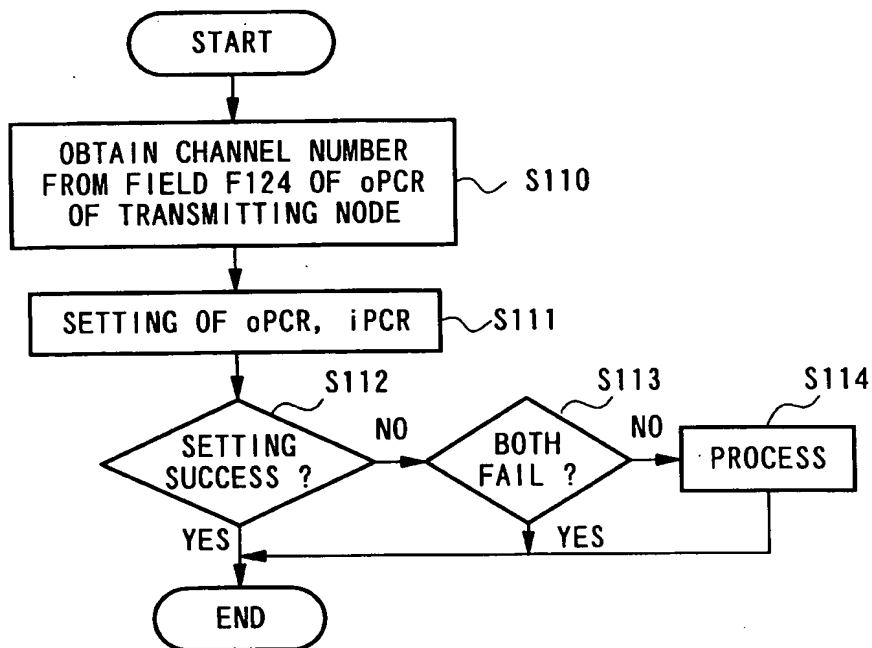


FIG. 28
PRIOR ART

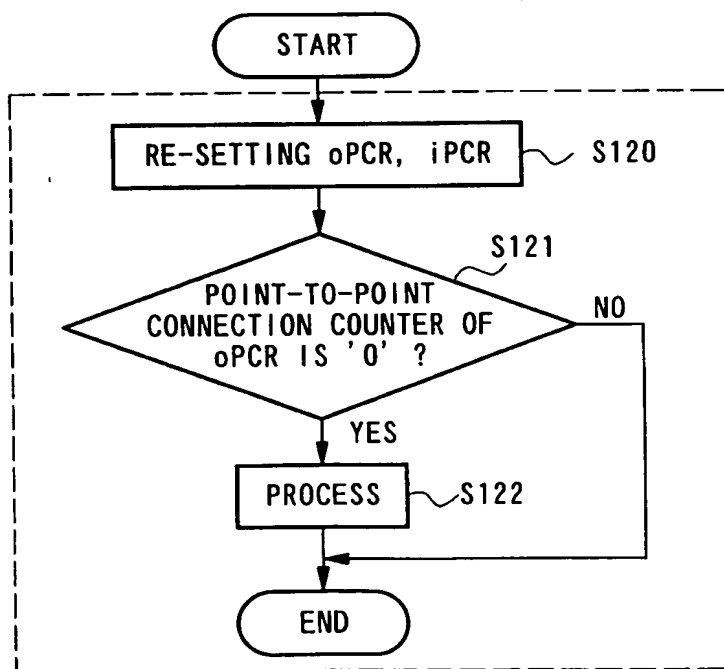


FIG. 29
PRIOR ART

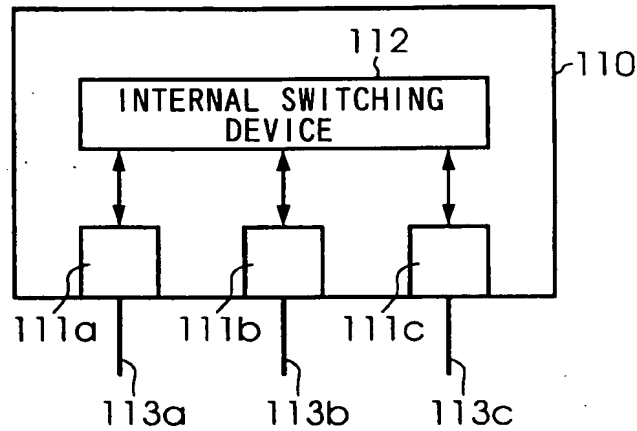


FIG. 30
PRIOR ART

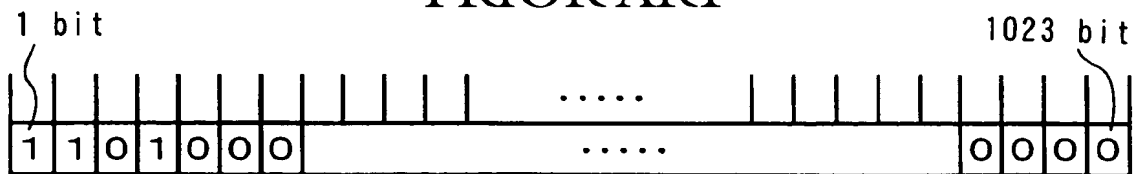


FIG. 31
PRIOR ART

